

ANNA UNIVERSITY - PRACTICAL EXAMINATIONS – APRIL – 2019

TENTATIVE TIME TABLE & PANEL OF EXAMINERS

24.04.2019 to 26.04.2019

COLLEGE NAME: **2115 – PANIMALAR INSTITUTE OF TECHNOLOGY**

DEPARTMENT CONDUCTING THE LAB: **ELECTRONICS & COMMUNICATION ENGINEERING**

S.No	Degree and Branch of the Student	Sem	Date of Exam	No. of Candidates	Batches with time Schedule (3 Hrs. per Batch)	Subject Code	Name of the practical Subject (Lab Venue)	Internal Examiner (With Designation, Dep.)				
1.	B.E., ECE (184)	IV	24.04.2019	62	Batch A – 9.00 -12.00 pm (31)	EC8461	Circuits Design & Simulation Laboratory (EDC Lab)	Mrs. M. Jayashree (Asst. Prof./ ECE)				
					Batch B – 1.00 - 4.00pm (31)							
			25.04.2019	61	Batch C – 9.00 -12.00 pm (31)			EC8461	Circuits Design & Simulation Laboratory (EDC Lab)	Dr. S. Sathiyapriya (Prof./ ECE)		
					Batch D – 1.00 - 4.00pm (30)							
			26.04.2019	61	Batch E – 9.00 -12.00 pm (31)						EC8461	Circuits Design & Simulation Laboratory (EDC Lab)
					Batch F – 1.00 - 4.00pm (30)							
2.	B.E., ECE (184)	IV	24.04.2019	61	Batch E – 9.00 -12.00 pm (31)	EC8462	Linear Integrated Circuit Laboratory (Communication Systems Lab)			Mr. D. Badri Narayanan (Asst. Prof./ ECE)		
					Batch F – 1.00 - 4.00pm (30)							
			25.04.2019	62	Batch A – 9.00 -12.00 pm (31)			EC8462	Linear Integrated Circuit Laboratory (Communication Systems Lab)	Dr. M. Prem Kumar (Prof./ ECE)		
					Batch B – 1.00 - 4.00pm (31)							
			26.04.2019	61	Batch C – 9.00 -12.00 pm (31)						EC8462	Linear Integrated Circuit Laboratory (Communication Systems Lab)
					Batch D – 1.00 - 4.00pm (30)							

EC8461 – Circuits & Simulation Integrated Laboratory & EC8462 – Linear Integrated Circuit Laboratory

Batch A: 211517106001 – 211517106032 (31)

Batch D: 211517106097 – 211517106126 (30)

Batch B: 211517106033 – 211517106064 (31)

Batch E: 211517106127 – 211517106157 (31)

Batch C: 211517106065 – 211517106096 (31)

Batch F: 211517106158 – 211517106189, 6301 (30)

PRINCIPAL