

ANNA UNIVERSITY - PRACTICAL EXAMINATIONS - OCT- 2017 (SLOT - II)

TENTATIVE TIME TABLE & PANEL OF EXAMINERS

26.10.2017 to 28.10.2017

COLLEGE NAME: 2115 – PANIMALAR INSTITUTE OF TECHNOLOGY

DEPARTMENT CONDUCTING THE LAB: ELECTRONICS & COMMUNICATION ENGG.

| S.No | Degree and Branch | Sem | Date of Exam | No. Of Candidates | Batches with time Schedule (3 Hrs. per Batch) | Subject Code | Name of the practical Subject (Venue) | Internal Examiner (With Designation, Dep.) |
|------|-------------------|-----|--------------|-------------------|---|--------------|--|---|
| 1 | B.E.,ECE (184) | III | 26.10.2017 | 69 | Batch A – 8.00 – 11.00 am (23) Batch B – 10.00 – 1.00 pm (23) Batch C – 2.00 – 5.00 pm (23) | EC6311 | Analog and Digital Circuits Laboratory (EDC Lab) | Mrs. V. Jeyaramya Asst. Prof. (G – 1)/ ECE |
| | | | 27.10.2017 | 69 | Batch D – 8.00 – 11.00 am (23) Batch E – 10.00 – 1.00 pm (23) Batch F – 2.00 – 5.00 pm (23) | | | Mrs. M. Jansi Rani Asst. Prof./ ECE |
| | | | 28.10.2017 | 46 | Batch G – 8.00 – 11.00 am (23) Batch H – 10.00 – 1.00 pm (23) | | | |
| 2 | B.E.,ECE (184) | III | 26.10.2017 | 69 | Batch D – 8.00 – 11.00 am (23) Batch E – 10.00 – 1.00 pm (23) Batch F – 2.00 – 5.00 pm (23) | EC6312 | OOPS and DS Lab (Networks Lab) | Mr. V. Gokula Krishnan Asst. Prof./ CSE |
| | | | 27.10.2017 | 69 | Batch G – 8.00 – 11.00 am (23) Batch H – 10.00 – 1.00 pm (23) Batch A – 2.00 – 5.00 pm (23) | | | |
| | | | 27.10.2017 | 46 | Batch B – 8.00 – 11.00 am (23) Batch C – 10.00 – 1.00 pm (23) | | OOPS and DS Lab (FOSS Lab) | Ms. S.M. Poonkuzhali Asst. Prof./ CSE |

II YR/ III SEM ECE : (ADC Lab, OOPS & DS Lab)

- Batch A :** 211516106001 – 6017, 6019 – 6024 (23)
Batch B : 211516106025 – 6028, 6030 – 6043, 6045 – 6049 (23)
Batch C : 211516106050 – 6066, 6068 – 6073 (23)
Batch D : 211516106074 – 6096 (23)
Batch E : 211516106097 – 6119 (23)
Batch F : 211516106120 – 6142 (23)
Batch G : 211516106143 – 6165 (23)
Batch H : 211516106166 – 6173, 6175 – 6182, 6184 – 6189, 6301 (23)

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|------|-------------------|-----|--------------|-------------------|---|--------------|--|---|
| 1 | B.E.,ECE (193) | V | 26.10.2017 | 88 | Batch D – 8.00 – 11.00 am (35) Batch E – 10.00 – 1.00 pm (18) Batch F – 2.00 – 5.00 pm (35) | EC6511 | Digital Signal Processing Laboratory (DSP Lab) | Mr. J. Venkatesan Asst. Prof. (G – 1)/ ECE |
| | | | 27.10.2017 | 105 | Batch A – 8.00 – 11.00 am (35) Batch B – 10.00 – 1.00 pm (35) Batch C – 2.00 – 5.00 pm (35) | | | Mrs. D. Kalaiarasi Asst. Prof./ ECE |
| 2 | B.E.,ECE (193) | V | 26.10.2017 | 105 | Batch A – 8.00 – 11.00 am (35) Batch B – 10.00 – 1.00 pm (35) Batch C – 2.00 – 5.00 pm (35) | EC6512 | Communication System Laboratory (Communication/ VLSI Lab) | Dr. S. Sathiya Priya Prof./ ECE |
| | | | 28.10.2017 | 88 | Batch D – 8.00 – 11.00 am (35) Batch E – 10.00 – 1.00 pm (18) Batch F – 2.00 – 5.00 pm (35) | | | Ms. S. Saranya Asst. Prof./ ECE |
| 3 | B.E.,ECE (193) | V | 27.10.2017 | 88 | Batch D – 8.00 – 11.00 am (35) Batch E – 10.00 – 1.00 pm (18) Batch F – 2.00 – 5.00 pm (35) | EC6513 | Microprocessor and Microcontroller Laboratory (Microprocessor Lab - I) | Mrs. R. Krishnaveni Asst. Prof./ ECE |
| | | | 28.10.2017 | 105 | Batch A – 8.00 – 11.00 am (35) Batch B – 10.00 – 1.00 pm (35) Batch C – 2.00 – 5.00 pm (35) | | | Mrs. K. Banumathi Asst. Prof./ ECE |

III YR/ V SEM ECE: (DSP, CS & μ P Lab)

- Batch A :** 211515106001 – 6014, 6017 – 6037 (35)
- Batch B :** 211515106038 – 6072 (35)
- Batch C :** 211515106073 – 6080, 6082 – 6093, 6095 – 6109 (35)
- Batch D :** 211515106110 – 6136, 6138 – 6145 (35)
- Batch E :** 211515106146 – 6163 (18)
- Batch F :** 211515106164 – 6181, 6183 – 6188, 6301, 6303 – 6311, 6501 (35)

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| S.No | Degree and Branch | Sem | Date of Exam | No. Of Candidates | Batches with time Schedule (3 Hrs. per Batch) | Subject Code | Name of the practical Subject (Venue) | Internal Examiner (With Designation, Dep.) |
|------|-------------------|-----|--------------|-------------------|---|--------------|---|--|
| 1 | B.E.,ECE (209) | VII | 26.10.2017 | 70 | Batch A – 8.00 – 11.00 am (24) Batch B – 10.00 – 1.00 pm (23) Batch C – 2.00 – 5.00 pm (23) | EC6711 | Embedded Laboratory (Microprocessor Lab - II) | Dr. M. Prem Kumar Prof./ ECE |
| | | | 27.10.2017 | 70 | Batch D – 8.00 – 11.00 am (24) Batch E – 10.00 – 1.00 pm (23) Batch F – 2.00 – 5.00 pm (23) | | | |
| | | | 28.10.2017 | 69 | Batch G – 8.00 – 11.00 am (23) Batch H – 10.00 – 1.00 pm (23) Batch I – 2.00 – 5.00 pm (23) | | | Dr. S. Ganesh Prof./ ECE |
| 2 | B.E.,ECE (209) | VII | 26.10.2017 | 70 | Batch D – 8.00 – 11.00 am (24) Batch E – 10.00 – 1.00 pm (23) Batch F – 2.00 – 5.00 pm (23) | EC6712 | Optical & Microwave Lab (Microwave Lab) | Mrs. R. Suganthi Asst. Prof. (G – 1)/ ECE |
| | | | 27.10.2017 | 69 | Batch G – 8.00 – 11.00 am (23) Batch H – 10.00 – 1.00 pm (23) Batch I – 2.00 – 5.00 pm (23) | | | Mr. J. Navarajan Asst. Prof. (G – 1)/ ECE |
| | | | 28.10.2017 | 70 | Batch A – 8.00 – 11.00 am (24) Batch B – 10.00 – 1.00 pm (23) Batch C – 2.00 – 5.00 pm (23) | | | |

IV YR/ VII SEM ECE : (Embedded, Optical & Microwave Lab)

| | |
|---|-------------|
| Batch A : 211514106001 – 6024 | (24) |
| Batch B : 211514106025 – 6043, 6045 – 6048 | (23) |
| Batch C : 211514106049 – 6071 | (23) |
| Batch D : 211514106072 – 6095 | (24) |
| Batch E : 211514106096 – 6118 | (23) |
| Batch F : 211514106119 – 6126, 6128 – 6142 | (23) |
| Batch G : 211514106143 – 6145, 6147 – 6166 | (23) |
| Batch H : 211514106167 – 6188, 6301 | (23) |
| Batch I : 211514106302 – 6323, 6501 | (23) |

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DEPARTMENT CONDUCTING THE LAB: ELECTRONICS & COMMUNICATION ENGG.

ARREAR LAB

| S.No | Degree and Branch | Sem | Date of Exam | No.Of Candidates | Batches with time Schedule (Reg. No.) | Subject Code | Name of the practical Subject (Lab Venue) | Internal Examiner (With Designation, Dep.) |
|------|-------------------|-----|--------------|------------------|---|--------------|---|---|
| 1 | B.E.,ECE (04) | IV | 26.10.2017 | 04 | Batch A – 2.00 – 5.00 pm (211515106006, 6007, 6056, 6140) | EC6411 | Circuits & Simulation Integrated Laboratory (EDC Lab) | Mrs. M. Jansi Rani Asst. Prof./ ECE |
| 2 | B.E.,ECE (07) | IV | 27.10.2017 | 07 | Batch A – 2.00 – 5.00 pm (211515106006, 6012, 6014, 6018, 6056, 6106, 6136) | EC6412 | Linear Integrated Circuit Laboratory (EDC Lab) | Mrs. R. Prathipa Asst. Prof. (G – 1)/ ECE |

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