

ANNA UNIVERSITY - PRACTICAL EXAMINATIONS, APRIL/ MAY – 2023 (SLOT - II)

TENTATIVE TIME TABLE & PANEL OF EXAMINERS

31.05.2023 to 03.06.2023

COLLEGE NAME: 2115 – PANIMALAR INSTITUTE OF TECHNOLOGY

DEPARTMENT CONDUCTING THE LAB: ELECTRONICS & COMMUNICATION ENGG.

S.No	Degree and Branch of the student (Strength)	Sem	Date of Exam	No. of Candidates	Batches with time Schedule (3 Hrs. per Batch)	Subject Code	Name of the practical Subject (Venue)	Internal Examiner (With Designation, Dep.)
1	B.E., ECE (182)	IV	31-05-2023	91	Batch A – 8.00am - 11.00am (31) Batch B – 10.30am - 1.30pm (30) Batch C – 1.00pm - 4.00pm (30)	EC3401	Networks and Security (Networks Lab)	Dr. R. Suganthi (Assoc. Prof./ ECE) & Mrs. R. Mary Victoria (Assoc. Prof./ ECE)
			01-06-2023	91	Batch D – 8.00am - 11.00am (31) Batch E – 10.30am - 1.30pm (30) Batch F – 1.00pm - 4.00pm (30)			
2	B.E., ECE (182)	IV	31-05-2023	91	Batch D – 8.00am - 11.00am (31) Batch E – 10.30am - 1.30pm (30) Batch F – 1.00pm - 4.00pm (30)	EC3462	Linear Integrated Circuits Laboratory (LIC Lab)	Dr. S. Sathiya Priya (Prof./ ECE) & Mr. M. Arun (Asst. Prof./ ECE)
			01-06-2023	91	Batch A – 8.00am - 11.00am (31) Batch B – 10.30am - 1.30pm (30) Batch C – 1.00pm - 4.00pm (30)			

III YR/ VI SEM ECE: (Networks and Security & LIC Lab)

Batch A : 211521106001 – 6005, 6007 – 6023, 6025 – 6033 (31)

Batch B : 211521106034 – 6063 (30)

Batch C : 211521106064 – 6093 (30)

Batch D : 211521106094 – 6124 (31)

Batch E : 211521106125 – 6148, 6150 – 6155 (30)

Batch F : 211521106156 – 6183, 6301, 6302 (30)

PRINCIPAL

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1	B.E., ECE (182)	IV	02-06-2023	91	Batch A – 8.00am - 11.00am (31) Batch B – 10.30am - 1.30pm (30) Batch C – 1.00pm - 4.00pm (30)	EC3461	Communication Systems Laboratory (VLSI Lab)	Dr. M. Hari Prasath (Asst. Prof./ ECE) & Dr. I. Poonguzhali (Assoc. Prof./ ECE)
			03-06-2023	91	Batch D – 8.00am - 11.00am (31) Batch E – 10.30am - 1.30pm (30) Batch F – 1.00pm - 4.00pm (30)			
2	B.E., ECE (182)	IV	02-06-2023	91	Batch D – 8.00am - 11.00am (31) Batch E – 10.30am - 1.30pm (30) Batch F – 1.00pm - 4.00pm (30)	EC3492	Digital Signal Processing (DSP Lab)	Mrs. V. Jeyaramya (Assoc. Prof./ ECE) & Mrs. R. Prathipa (Assoc. Prof./ ECE)
			03-06-2023	91	Batch A – 8.00am - 11.00am (31) Batch B – 10.30am - 1.30pm (30) Batch C – 1.00pm - 4.00pm (30)			

III YR/ VI SEM ECE: (Communication Systems & DSP Lab)

Batch A : 211521106001 – 6005, 6007 – 6023, 6025 – 6033 (31)
Batch B : 211521106034 – 6063 (30)
Batch C : 211521106064 – 6093 (30)
Batch D : 211521106094 – 6124 (31)
Batch E : 211521106125 – 6148, 6150 – 6155 (30)
Batch F : 211521106156 – 6183, 6301, 6302 (30)

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DEPARTMENT CONDUCTING THE LAB: **ELECTRONICS & COMMUNICATION ENGG.**

S. No.	Degree and Branch of the student (Strength)	Sem	Date of Exam	No. of Candidates	Batches with time Schedule (3 Hrs. per Batch)	Subject Code	Name of the practical Subject (Venue)	Internal Examiner (With Designation, Dep.)
1	B.E., ECE (115)	VI	31-05-2023	58	Batch A – 8.30am - 11.30am (29) Batch B – 12.00pm - 3.00pm (29)	EC8661	VLSI Design Laboratory (VLSI Lab)	Dr. A. Selvarani (Prof./ ECE)
			01-06-2023	57	Batch C – 8.30am - 11.30am (29) Batch D – 12.00pm - 3.00pm (28)			
2	B.E., ECE (115)	VI	31-05-2023	57	Batch C – 8.30am - 11.30am (29) Batch D – 12.00pm - 3.00pm (28)	EC8681	Microprocessors & Microcontrollers Laboratory (µP Lab)	Mr. L. Ashok Kumar (Assoc. Prof./ ECE)
			01-06-2023	58	Batch A – 8.30am - 11.30am (29) Batch B – 12.00pm - 3.00pm (29)			
3	B.E., ECE (115)	VI	02-06-2023	58	Batch A – 9.30am - 12.30am (29) Batch B – 1.00pm - 4.00pm (29)	HS8581	Professional Communication (Communication skills Lab)	Mrs. E. Pearlin (Asst. Prof. / ENG)
				57	Batch C – 9.30am - 12.30am (29) Batch D – 1.00pm - 4.00pm (28)			Dr. L. Mayavan (Assoc. Prof./ ENG)

II YR/ IV SEM ECE:

Batch A : 211520106001 – 6011, 6013 – 6019, 6021 – 6031 (29)

Batch B : 211520106032 – 6060 (29)

Batch C : 211520106061 – 6089 (29)

Batch D : 211520106090 – 6101, 6103 – 6109, 6301 – 6309 (28)

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DEPARTMENT CONDUCTING THE LAB: ELECTRONICS & COMMUNICATION ENGG.

ARREAR LAB

S.No.	Degree and Branch	Sem	Date of Exam	No. of Candidates	Batches with time Schedule (3 Hrs. per Batch)	Subject Code	Name of the practical Subject (Venue)	Internal Examiner (Designation, Dep.)
1	B.E., ECE (06)	III	02-06-2023	06	Batch A – 8.30 – 11.30 am (06)	EC3352	Digital Systems Design (LIC Lab)	Mrs. R. Mary Victoria (Asst. Prof./ ECE)

II YR/ 03 SEM ECE: (DSD Lab)

Batch A: 211521106066, 6075, 6083, 6098, 6166, 6302 (06)

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